

IN THE UNITED STATES PATENT OFFICE

Serial No.: Not yet assigned

Filed: Herewith

For: WIRING SUBSTRATE, METHOD OF MANUFACTURING THE SAME
AND SEMICONDUCTOR DEVICE

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Atty Doc. No.: 89-01

J1017 U.S. PTO
09/826512
04/05/01

#4
8/26/01
M. P. Hughes

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In the interest of full disclosure, the following items are herewith identified in Form PTO-1449 and a copy of the same is hereby provided, for the convenience of the U.S. Patent and Trademark Office.

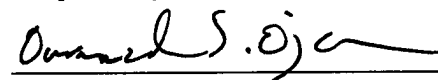
This Information Disclosure Statement is being filed before the mailing date of the first official action.

FOREIGN PATENTS

(AA) **JP-5-47842** Miyamoto Toshio, dated February 26, 1993. For relevance see English abstract.

(AB) **JP-10-270496** Imasu Seishi, dated October 9, 1998. For relevance see English abstract.

Respectfully submitted,



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(Use several sheets if necessary)

Application Number
Not yet assigned

Applicant(s)
Michio Horiuchi, Takashi Kurihara

Filing Date

Herewith

Group Art Unit unassigned

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

The graph shows a decreasing curve starting at (0, 10) and approaching the x-axis as the number of people increases. The x-axis is labeled 'Number of people' and the y-axis is labeled 'Number of people who are not in the room'.

EXAMINER

DATE CONSIDERED

3/20/02

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